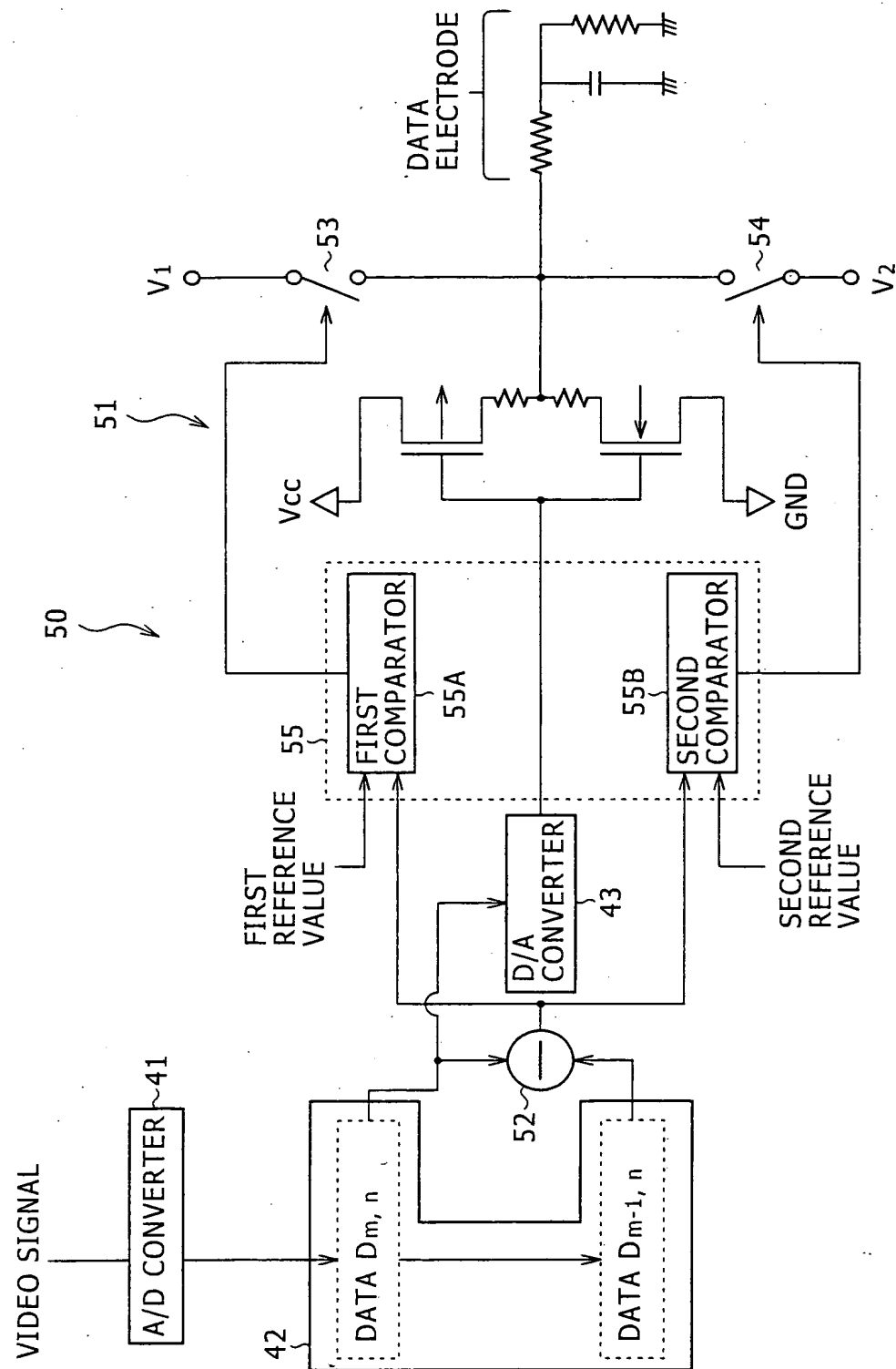


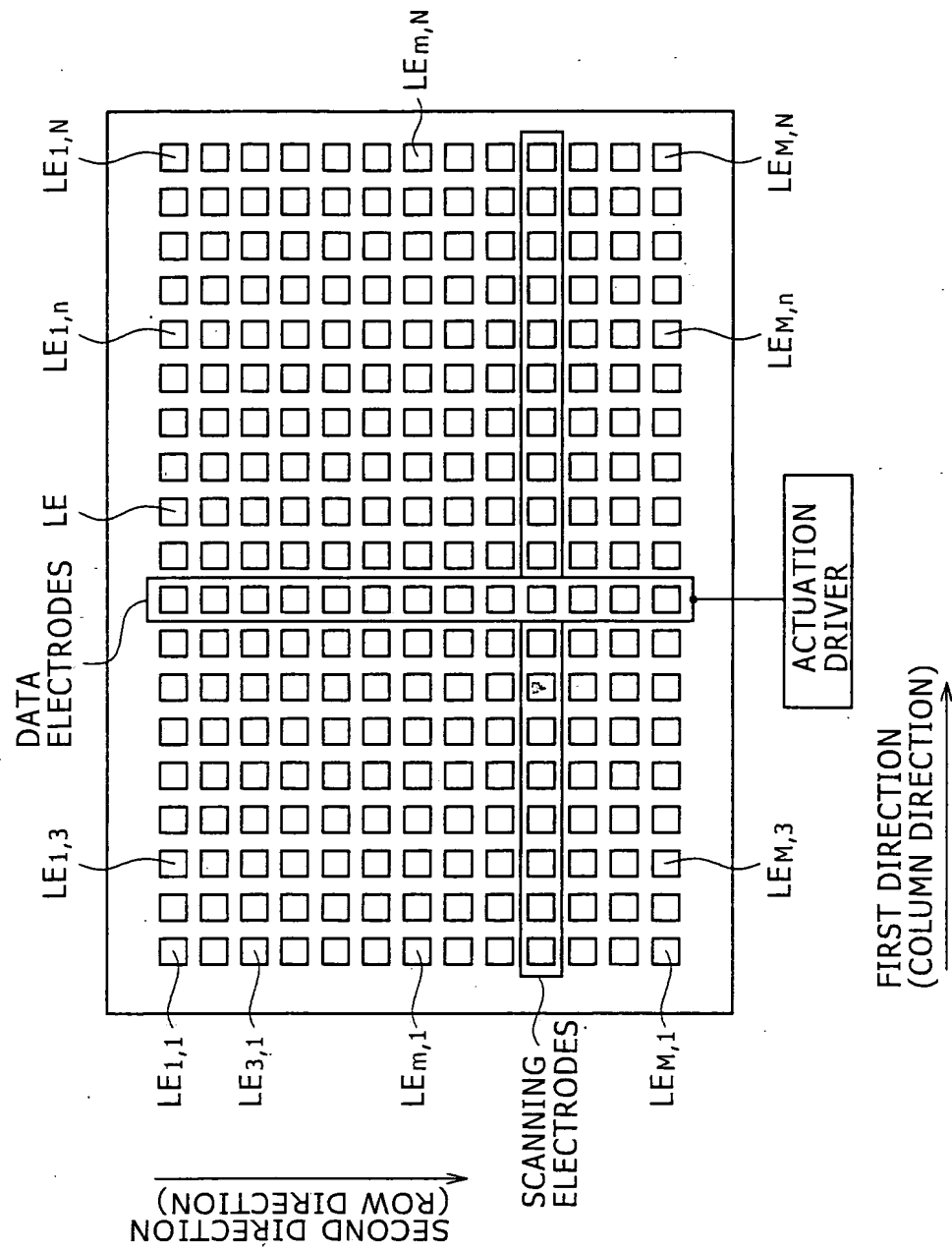
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FIG. 1



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FIG. 2



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FIG. 3A

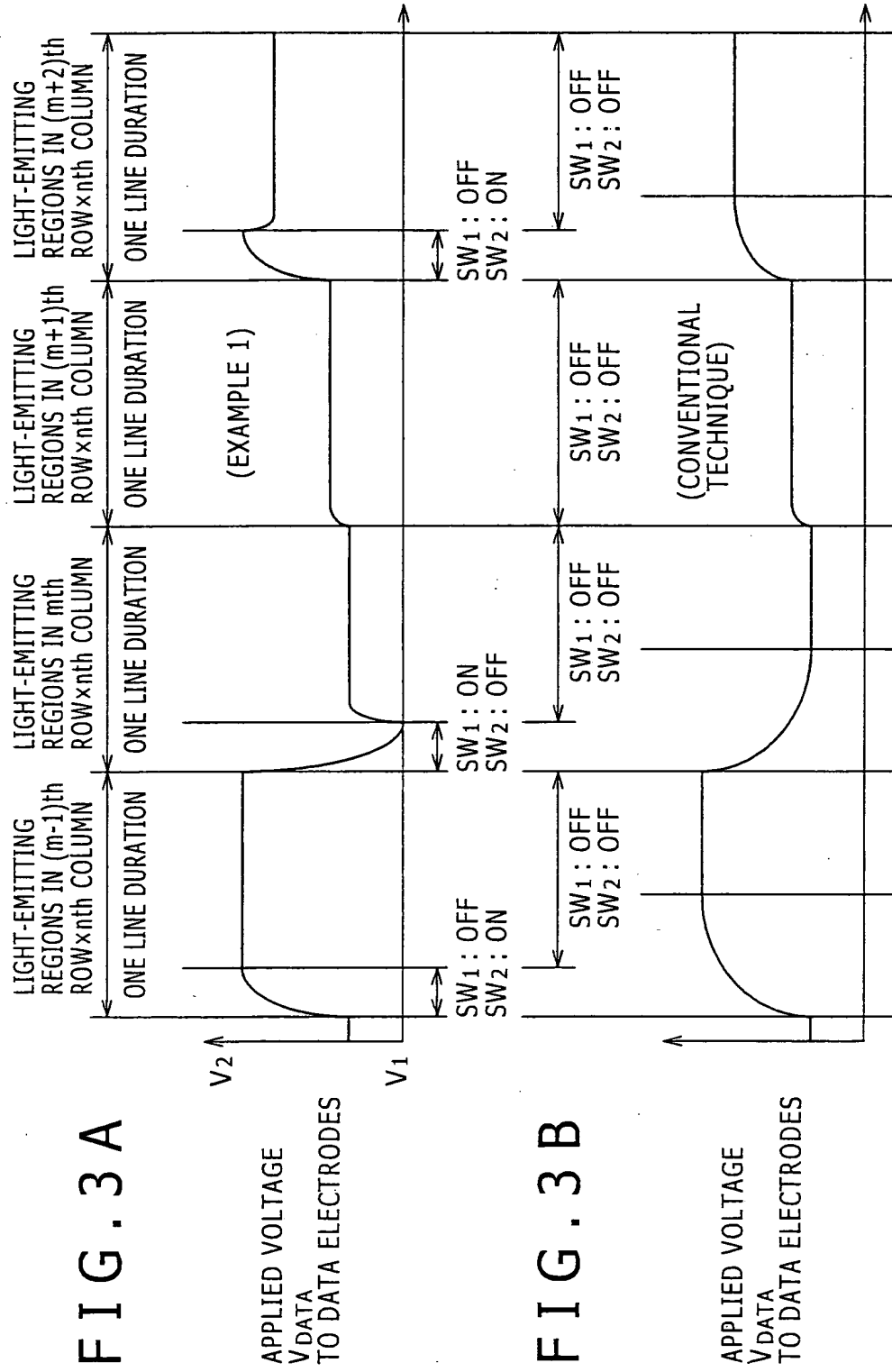
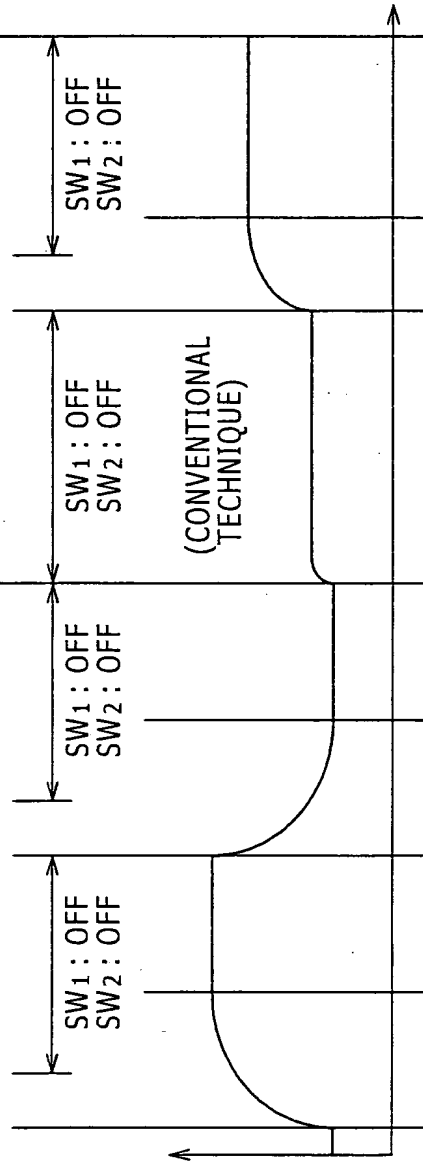


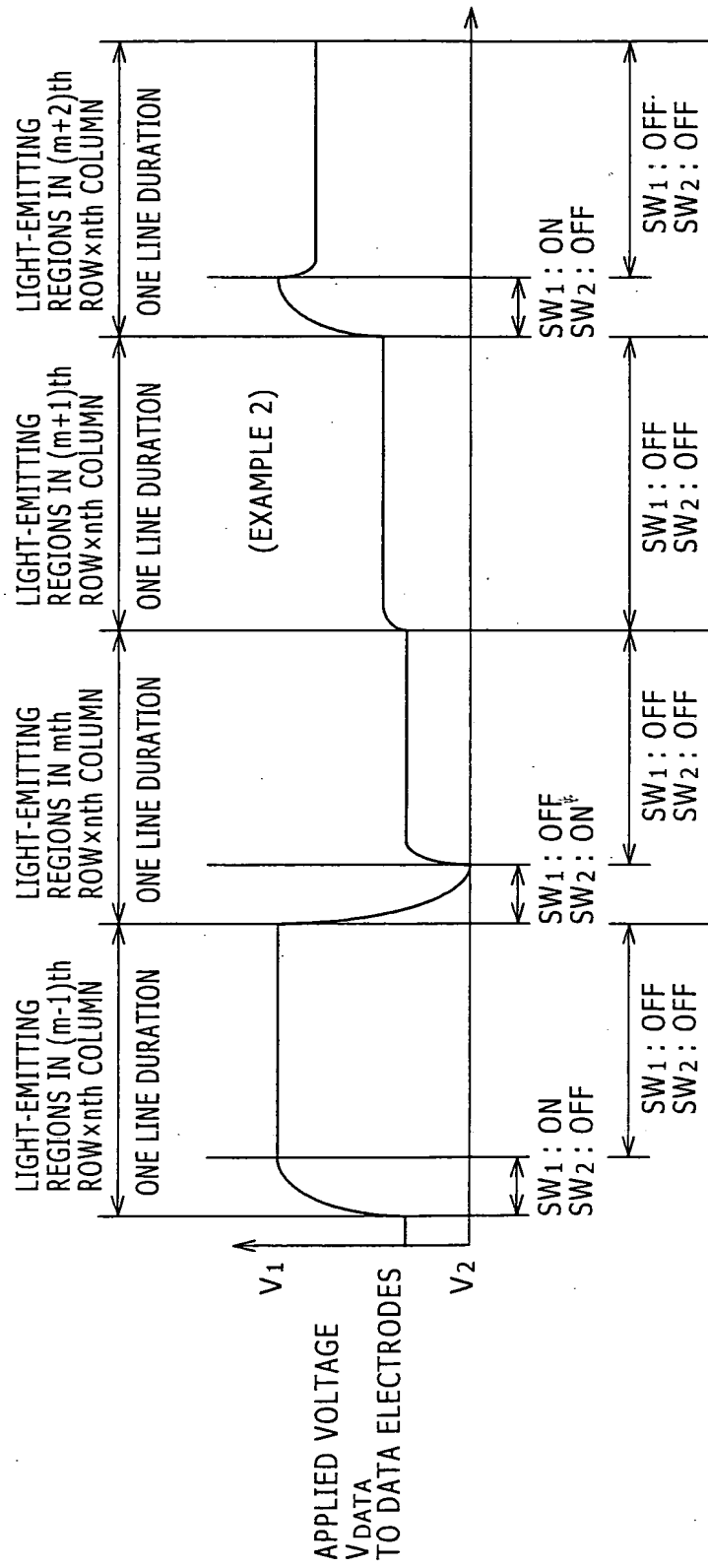
FIG. 3B

APPLIED VOLTAGE
 V_{DATA}
TO DATA ELECTRODES



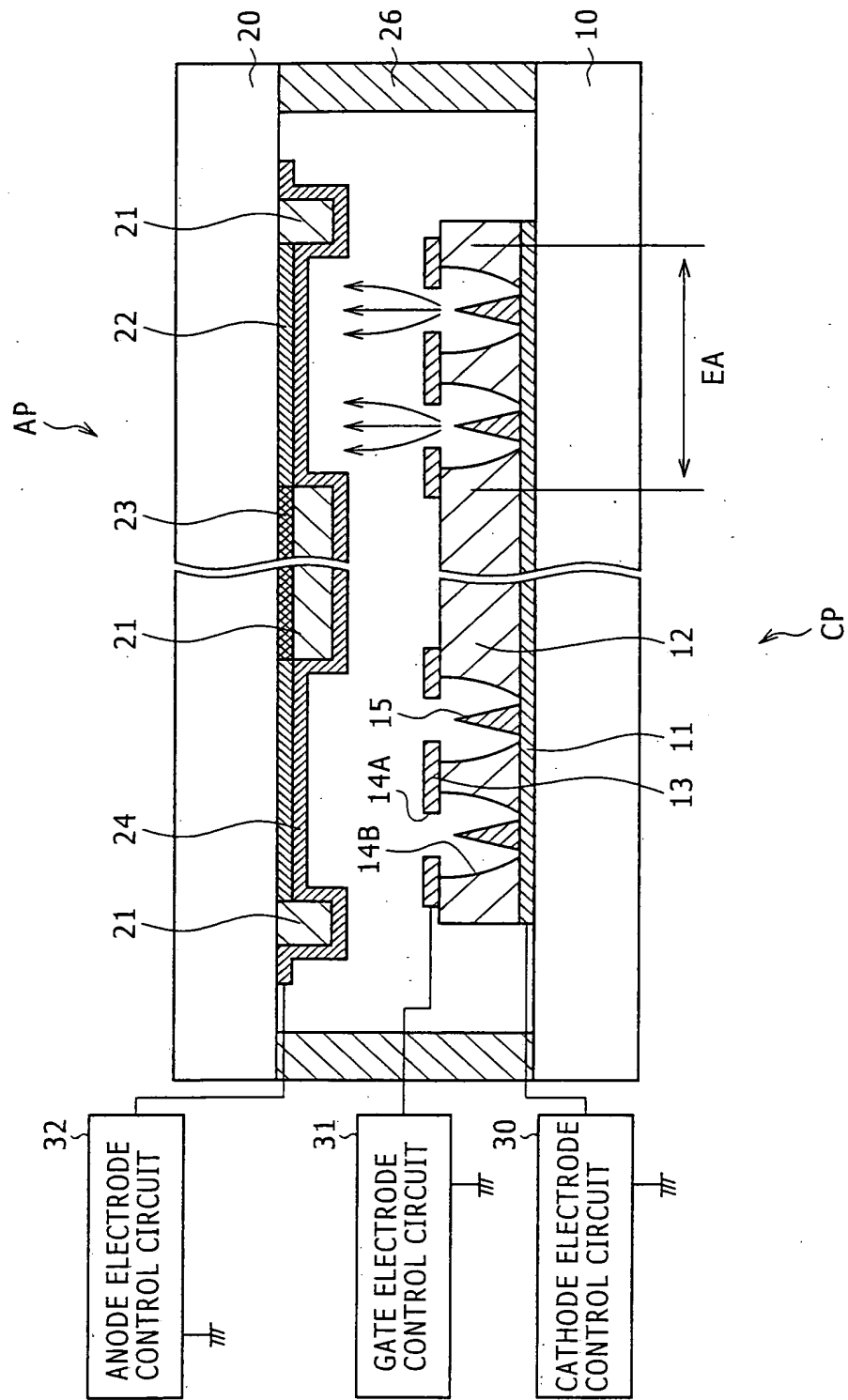
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FIG. 4



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FIG. 5



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FIG. 6

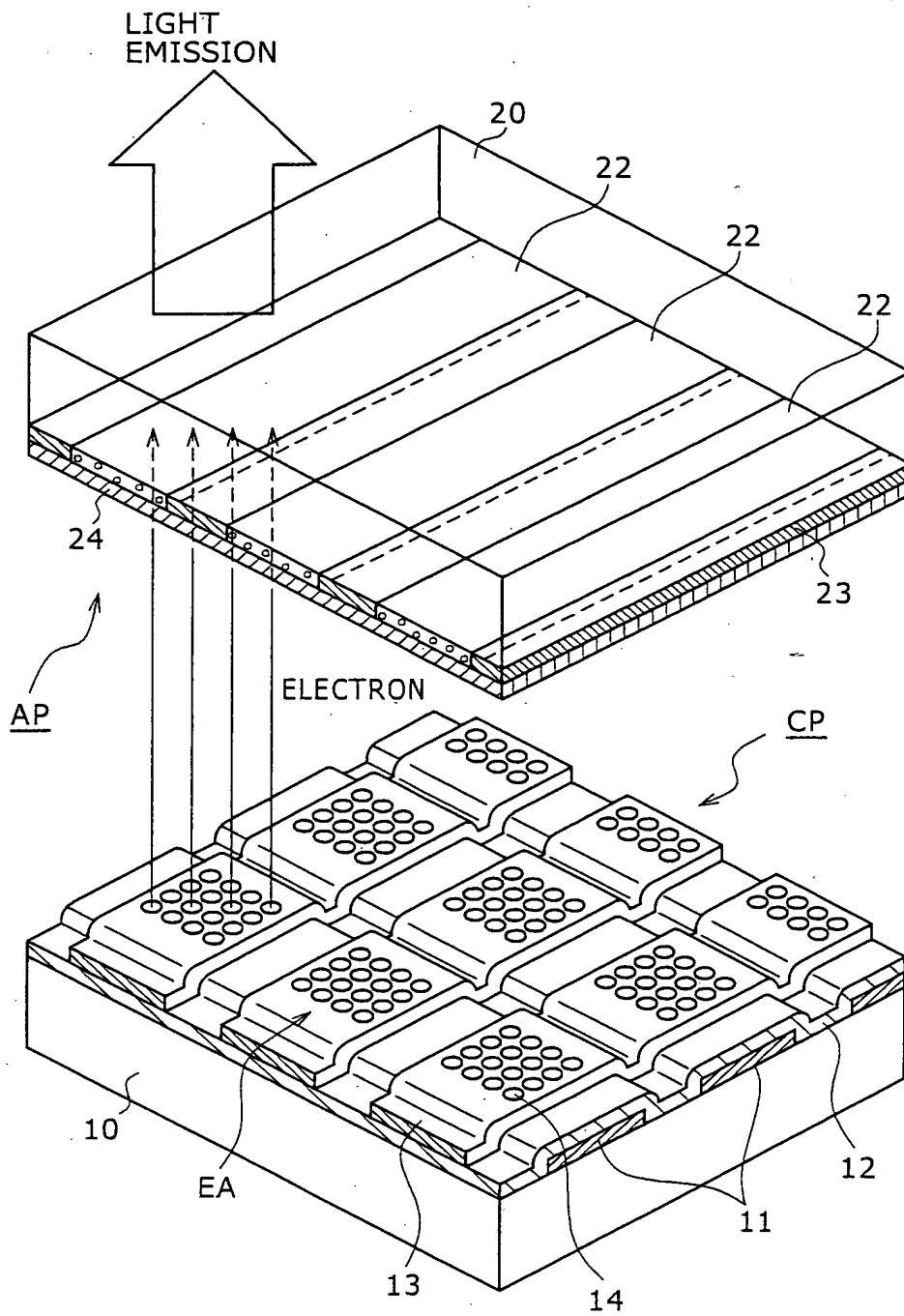


FIG. 7

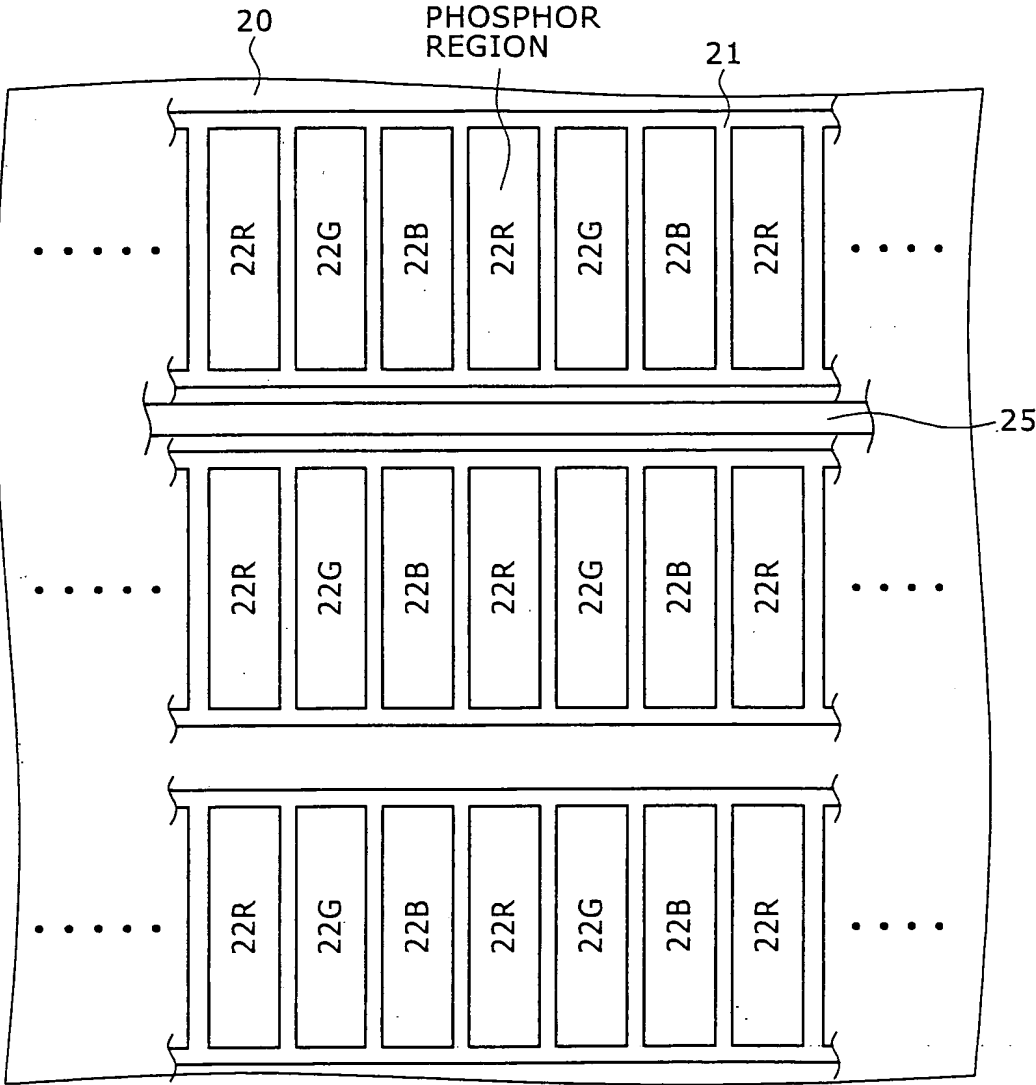


FIG. 8

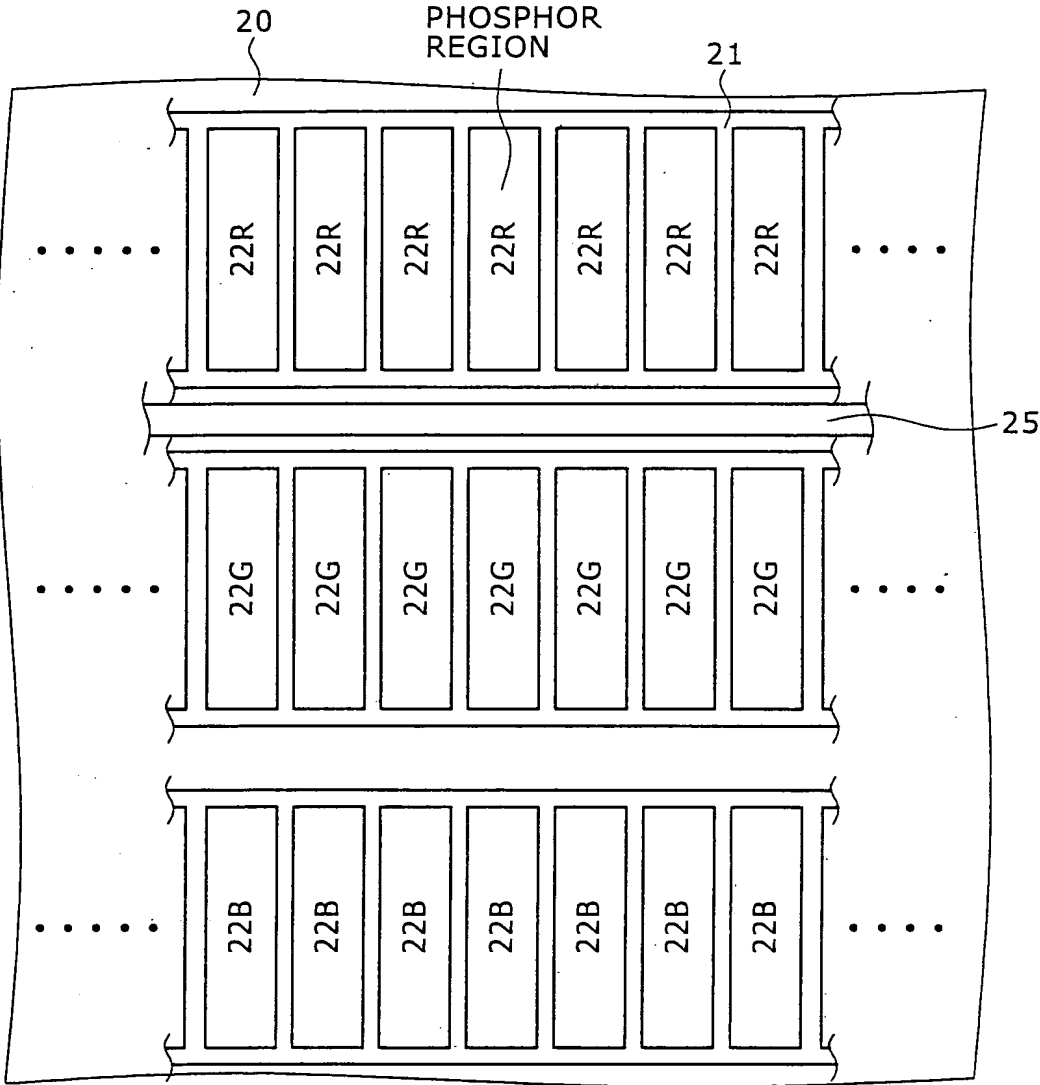
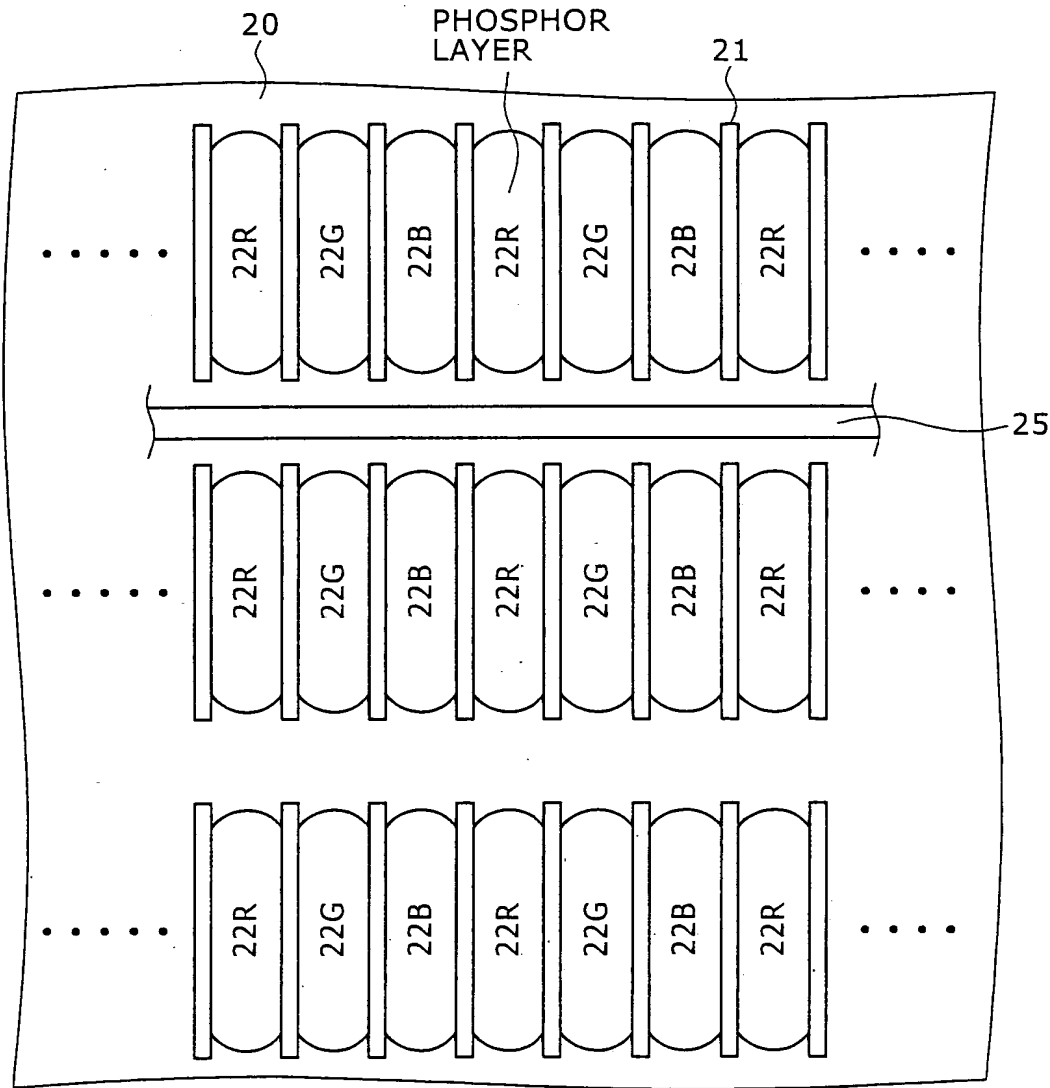
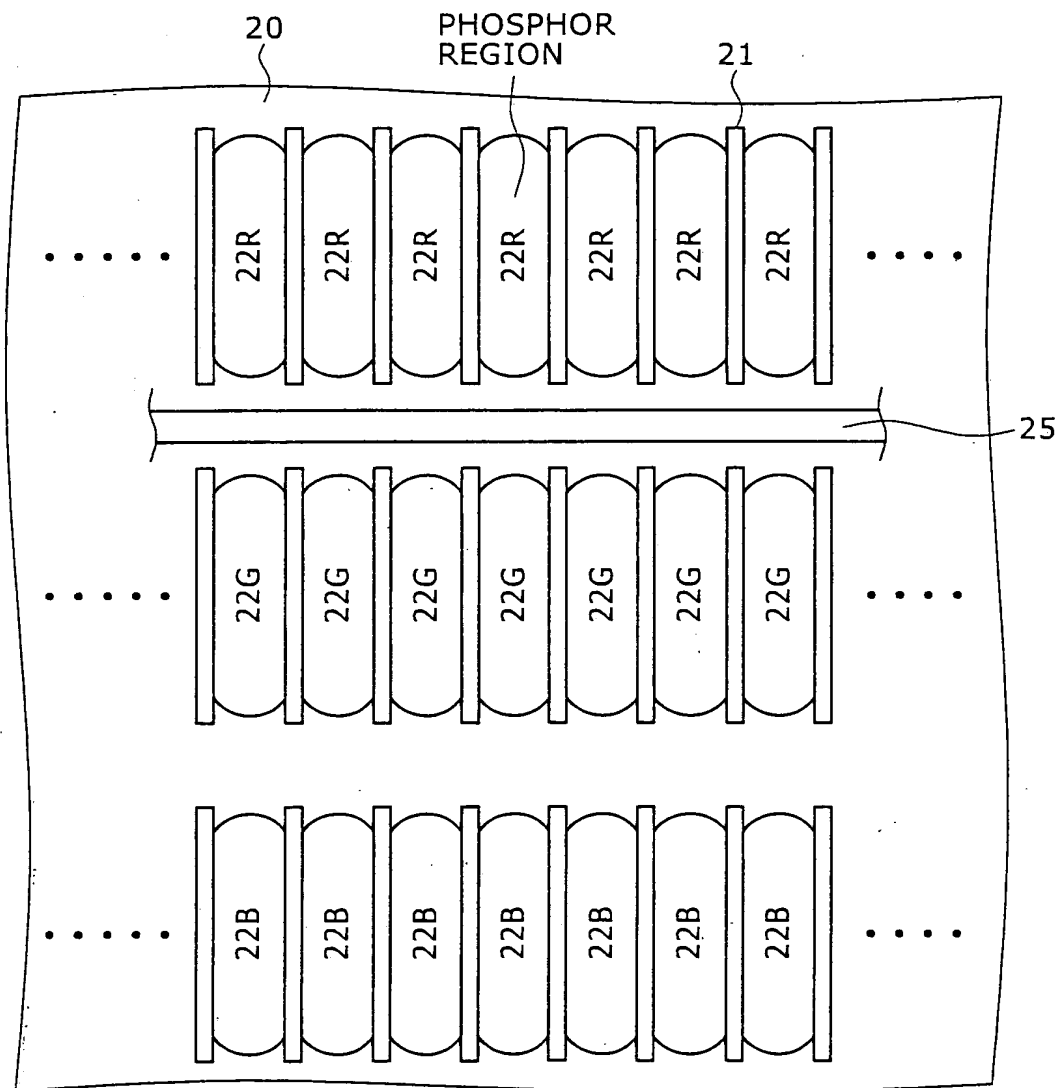


FIG. 9



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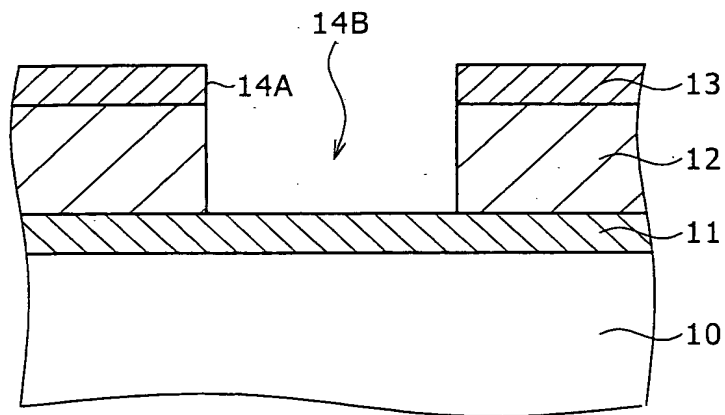
FIG. 10



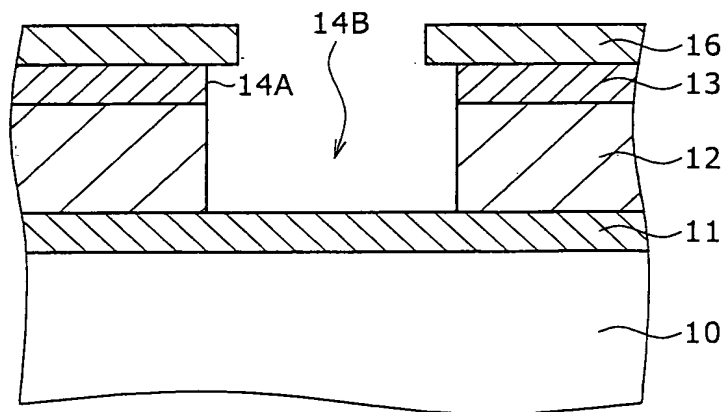
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FIG. 11 A

[STEP — A2]

**FIG. 11 B**

[STEP — A3]



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FIG. 12A

[STEP - A4]

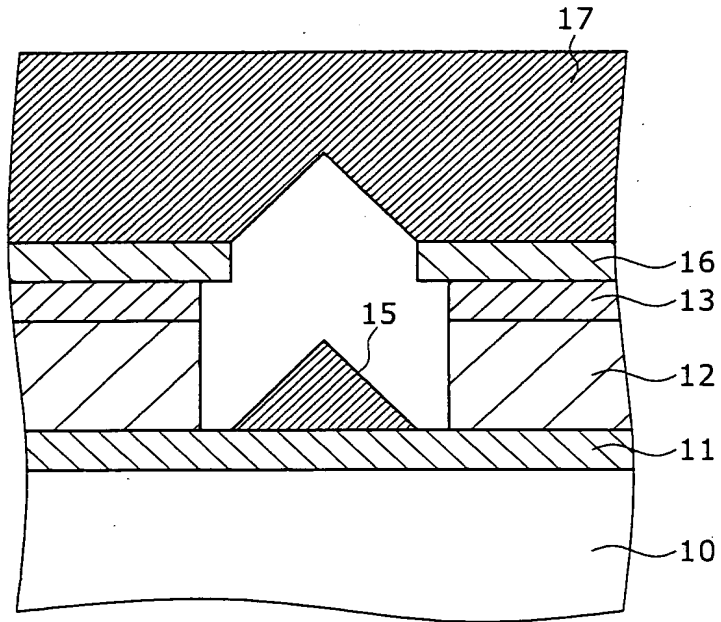
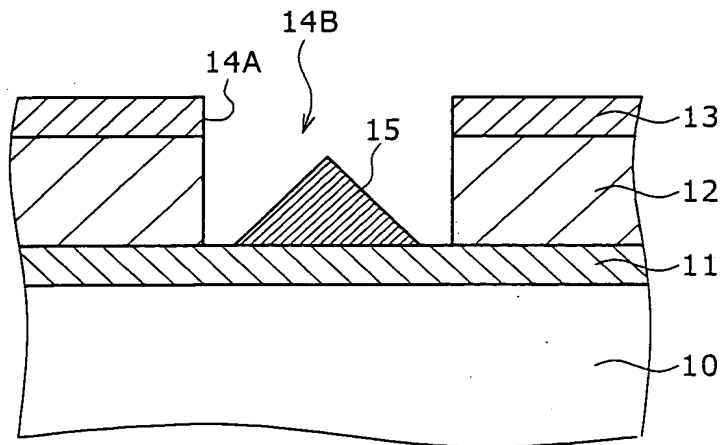


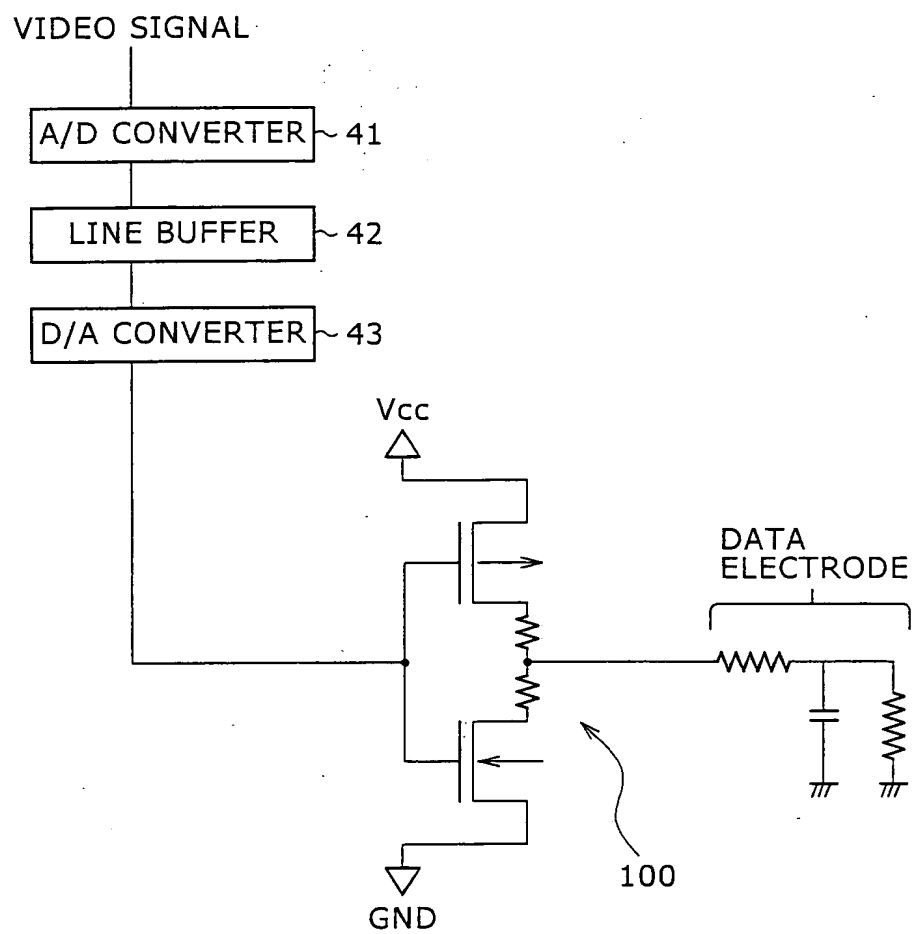
FIG. 12B

[STEP - A5]



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FIG. 14



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